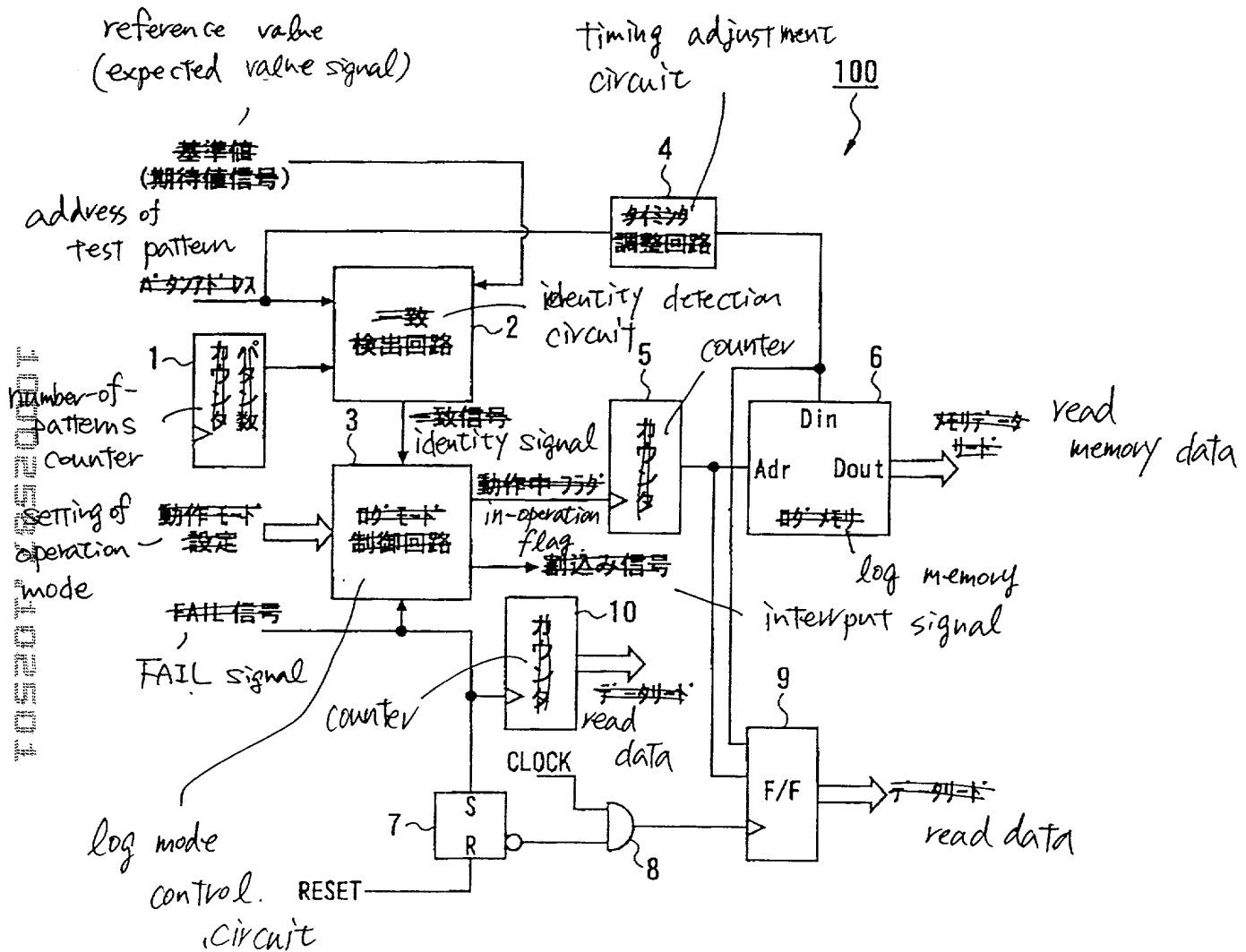


FIG. 1

1/5



to execute from address
next to previously terminated

read memory data

mode setting

read memory data

read memory data

出力 読出

設定

前回も言った次の7バイトから実行するよ
32ビット範囲を再指定

address

mode
mode setting

software processing

test start ~~test start~~

original

(c) ~~三致~~信号
identity signal

(d) ~~動作中~~ in-operation flag

interrupt signal

Proof - 2000/10/30

cycle corresponding to memory capacity

FIG. 4

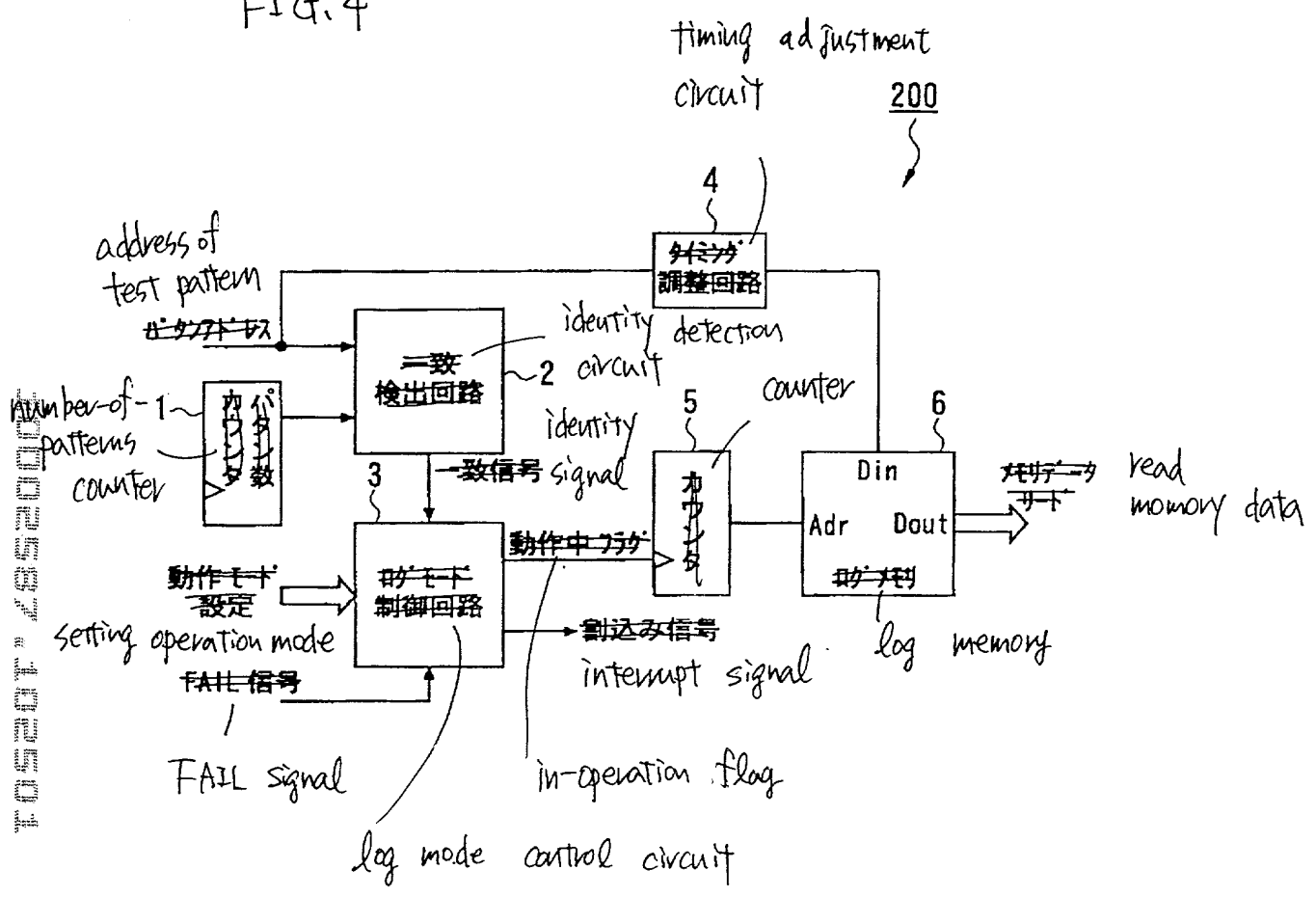


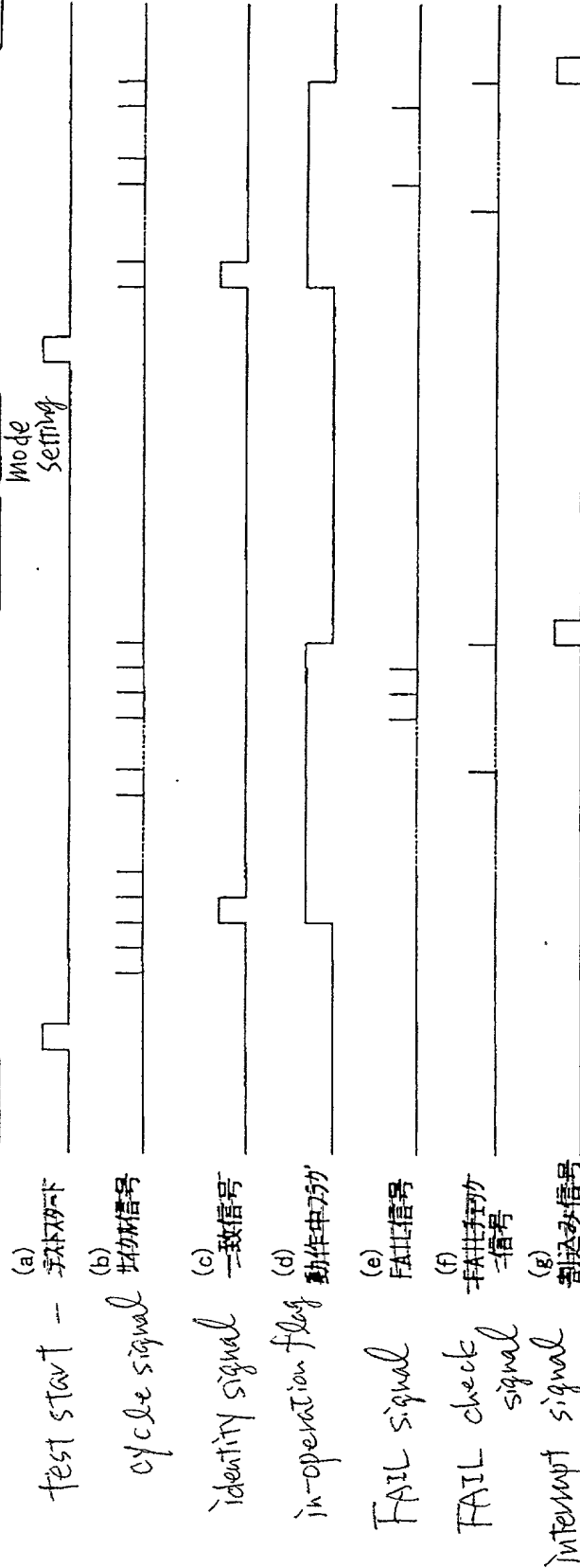
FIG. 5

reset range to execute
from address next to
previously terminated
address

reset range to execute
from address next to
previously terminated
address

read memory data
mode setting
read memory data
address read memory data

mode software processing
FAIL mode setting
mode setting



Proof - 2000/10/30

cycle corresponding to memory capacity
メモリ容量分の周期

cycle corresponding to memory capacity
メモリ容量分の周期

run till FAIL is detected because there was no FAIL at the point of time when memory capacity had been occupied